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A diagram of a 6-bit shift register. It consists of six rectangular cells arranged in a horizontal row, connected by a single horizontal line. Each cell has an input on the left and an output on the right. The inputs are labeled A, B, C, D, E, and F from left to right. The outputs are also labeled A, B, C, D, E, and F from left to right. The first cell (A) has a feedback loop from its output back to its input. The second cell (B) has a feedback loop from its output back to its input. The third cell (C) has a feedback loop from its output back to its input. The fourth cell (D) has a feedback loop from its output back to its input. The fifth cell (E) has a feedback loop from its output back to its input. The sixth cell (F) has a feedback loop from its output back to its input.

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